

<b>Course Code:4IT209PC</b>	<b>Course Title: Data Structures</b>	<b>LTPC: L-3 T-0 P-0 C-3</b>
Course Prerequisite: Fundamentals of programming language & logic building skills		
Course Objectives:	<ol style="list-style-type: none"> <li>1. To understand fundamental data structures.</li> <li>2. To perform different operations on linear and multidimensional arrays and various operations on it.</li> <li>3. To study the aspects of linked lists and explore various operations performed on it.</li> <li>4. To explore different operations on stack and queues along with various operations on it.</li> <li>5. To analyze the fundamental concepts of tree-based data structures.</li> <li>6. To study and implement the fundamental concepts of graphs and their representations.</li> </ol>	
Course Outcomes (Expected Outcome):	<p>On completion of the course, the students will be able to:</p> <ol style="list-style-type: none"> <li>1. Describe fundamental data structures, their operations, algorithmic complexity, string processing techniques and pattern matching algorithms.</li> <li>2. Explain linear and multidimensional arrays and various operations on it.</li> <li>3. Examine different types of linked list, their memory representation and operations.</li> <li>4. Analyze stack and queues data structures, their operations and applications.</li> <li>5. Evaluate tree-based data structure such as binary tree, binary search trees, heaps etc.</li> <li>6. Design graph and graph traversal and various sorting techniques.</li> </ol>	
<b>Unit I:</b>	<b>Introduction to Data Structures</b>	<b>Hours: 7</b>
Introduction to Data structures, Data Structure Operations, Algorithmic Notation, Complexity of algorithms. String processing: storing strings, character data type, string operations, word processing, and pattern matching algorithms.		
<b>Unit II:</b>	<b>Arrays &amp; Record Structure</b>	<b>Hours: 7</b>
Linear arrays: Memory Representation of arrays, traversing linear arrays, insertion & deletion operations, Bubble sort, Linear search and Binary search algorithms. Multi-dimensional arrays, Pointer arrays. Record structures.		
<b>Unit III:</b>	<b>Linked lists</b>	<b>Hours: 7</b>
Linked lists: Memory Representation of Linked List, traversing a linked list, searching a linked list. Memory allocation & garbage collection. Insertion & deletion operations on linked lists. Header linked lists, Two- way linked lists.		
<b>Unit IV:</b>	<b>Stacks &amp; Queues</b>	<b>Hours: 7</b>
Stacks: Sequential Memory Representation of Stack, Arithmetic expressions: Polish notation. Quick sort, Recursion, Tower of Hanoi. Queues: Sequential Memory Representation of Queue, Dequeue, Priority queues.		
<b>Unit V:</b>	<b>Trees</b>	<b>Hours: 7</b>
Introduction to Trees, Binary trees, Memory Representation of Binary Tree, Traversing binary trees, Header nodes, Binary Search Tree, Heap and heap sort, Path length & Huffman's algorithm.		
<b>Unit VI:</b>	<b>Graphs &amp; Sorting Algorithms</b>	<b>Hours: 7</b>
Introduction to Graphs, Memory representation of graphs, Warshalls' algorithm, operations on Graphs, Breadth First Search, Depth First Search. Sorting: Insertion Sort, Selection Sort, Radix sort, Merge Sort.		

**Textbooks:**

1. Seymour Lipschutz: Data Structures, Schaum's Outline Series, McGraw-Hill, International Editions.
2. Trembley, Sorenson: An Introduction to Data Structures with Applications, McGraw Hill.

**Reference Books:**

1. Ellis Horowitz, Sartaj Sahni: Fundamentals of Data Structures, CBS Publications.
2. Data Structure Using C, Balagurusamy
3. Standish: Data Structures in Java, Pearson Education.

<b>Course Code: 4IT210PC</b>	<b>Course Title: Data Communication &amp; Networking</b>	<b>LTPC: L-3, T-0, P-0, C-3</b>
Course Prerequisite:	Basic knowledge of electronics and computer fundamentals, binary systems, and an introductory understanding of networking concepts.	
Course Objectives:	<ol style="list-style-type: none"> <li>1. To provide foundational knowledge of data communication and networking, including essential protocols and concepts.</li> <li>2. To explain the OSI model, TCP/IP protocol suite, addressing, and communication protocols critical for network functionality.</li> <li>3. To study the roles and techniques of OSI layers, including physical, data link, network, transport, and application layers.</li> <li>4. To understand mechanisms like error detection, correction, flow control, and data link control for reliable communication.</li> <li>5. To analyze network layer protocols (IPv4, IPv6), routing, and data transmission strategies across networks.</li> <li>6. To explore transport and application layer protocols (TCP, UDP, DNS, HTTP) and their role in internet communication.</li> </ol>	
Course Outcomes (Expected Outcome):	<ol style="list-style-type: none"> <li>1. Describe the fundamental concepts of data communication, including components, types of data flow, and communication systems</li> <li>2. Compare the OSI and TCP/IP models by analyzing the functionalities and protocols at each layer.</li> <li>3. Apply knowledge of signal conversion methods and apply error detection and correction techniques to ensure reliable communication</li> <li>4. Analyze IPv4/IPv6 protocols, and packet delivery processes</li> <li>5. Compare and contrast TCP and UDP features, use cases, and transport layer mechanisms.</li> <li>6. Assess application layer services like DNS and HTTP and troubleshoot networking issues using protocols and addressing schemes.</li> </ol>	
<b>Unit I:</b>	<b>Data Communication Concepts</b>	<b>Hours: 7</b>
Data Communication: Components, Data Representation, Data Flow; Networks: Types of Connections, Topologies, Network Models; OSI Model; TCP/IP Protocol Suite; Addressing; Signals: Analog Signals, and Digital Signals; Transmission Media and Technologies.		
<b>Unit II:</b>	<b>Physical Layer</b>	<b>Hours:7</b>
Analog and Digital Data; Digital Transmission: Digital-to-Digital Conversion, Analog-to-Digital Conversion; Analog Transmission: Digital-to-Analog Conversion, Analog-to-Analog Conversion.		
<b>Unit III:</b>	<b>Data Link Layer</b>	<b>Hours:7</b>
Error Detection and Correction: Basic, Block Coding, Linear Block Codes, Cyclic Codes, Checksum; Data Link Control: Framing, Flow and Error Control; Protocols: Noiseless Channels, Noisy Channels, HDLC, P2P.		
<b>Unit IV:</b>	<b>Network Layer</b>	<b>Hours:7</b>
Logical Addressing: IPv4 Addresses, IPv6 Addresses; Address Mapping; ICMP; IGMP; Packet Delivery and Forwarding; Unicast Routing Protocols: Distance Vector, Link State; Multicast Routing		
<b>Unit V:</b>	<b>Transport Layer</b>	<b>Hours:7</b>
Process-to-Process Delivery; TCP/UDP Protocol Suite: UDP User Datagram with Operation and Uses, TCP Services with Features, Segment, Flow Control, and Error Control.		
<b>Unit VI:</b>	<b>Application Layer</b>	<b>Hours:7</b>
Upper OSI Layers: Session Layer, Presentation Layer, Application Layer functions and services, Domain Name System: Domain Name Space, Distribution, DNS in the Internet, Resolution		
<b>Text Book:</b> <ol style="list-style-type: none"> <li>1. "Data Communication and Networking" by Behrouz A. Forouzan, 5<sup>th</sup> Edition, McGraw-Hill Education</li> <li>2. "Data and Computer Communications" by William Stallings, 10<sup>th</sup> Edition, Pearson</li> <li>3. "Computer Networking: A Top-Down Approach" by James F. Kurose and Keith W. Ross, 8<sup>th</sup> Edition, Pearson</li> </ol>		

### **Reference Books**

1. "Computer Networks" by Andrew S. Tanenbaum and David J. Wetherall, 5th Edition, Pearson
2. "Networking Essentials" by Peter J. D. Smith, 4th Edition, McGraw-Hill Education
3. "Computer Networking: Principles, Protocols and Practice" by Olivier Bonaventure, 1st Edition, Self-published, available online (Open-source)
4. "TCP/IP Illustrated, Volume 1: The Protocols" by W. Richard Stevens, 2nd Edition, Addison-Wesley

<b>Course Code: 4IT211PC</b>	<b>Course Title: Computer Organization &amp; Architecture</b>	<b>LTPC: L-2, T-0, P-0, C-2</b>
Course Prerequisite:	Computer fundamentals, analog and digital circuits	
Course Objectives:	1. To understand how Computer Systems works & its organization. 2. To give methodical treatment of machine instructions and its execution. 3. To understand organization of I/O devices and concept of Bus Arbitration. 4. To learn about the organization of main memory. 5. To introduce the concept of Cache memory and Virtual Memory. 6. To understand and analyze the design of ALU.	
Course Outcomes (Expected Outcome):	After completion of this course students will be able to- <ol style="list-style-type: none"> <li>1. Describe the basic structure of computers including functional units, addressing modes and instruction sequencing.</li> <li>2. Compare the working of control unit like hardwired control and Micro-programmed control.</li> <li>3. Examine the different ways of communicating with i/o devices and standard i/o interfaces.</li> <li>4. Demonstrate the need of hierarchical memory system.</li> <li>5. Assess Virtual Memory and Multiprocessors</li> <li>6. Synthesize the basic knowledge of mathematics and logic circuits to analyze the design of ALU.</li> </ol>	
<b>Unit I:</b>	<b>Basic structure of computer</b>	<b>Hours:05</b>
Hardware & software, program sequencing. concept of memory locations & address. Main memory operation. Instructions & instruction sequencing. Addressing modes.		
<b>Unit II:</b>	<b>Processing Unit</b>	<b>Hours:05</b>
Fundamental concepts. Execution of complete instructions. Multiple –Bus Organization. Micro-programmed control; microinstructions.		
<b>Unit III:</b>	<b>I/O organization</b>	<b>Hours:05</b>
Accessing I/O devices, interrupts, direct memory access, bus arbitration: Centralized and Distributed.		
<b>Unit IV:</b>	<b>Memory Unit</b>	<b>Hours:05</b>
Basic concepts, semiconductor RAM memories, internal organization, static & dynamic RAMs, ROMs. speed, size & cost considerations.		
<b>Unit V:</b>	<b>Cache memories</b>	<b>Hours:05</b>
Cache Memories, Performance considerations. Virtual memories, address translation.		
<b>Unit VI:</b>	<b>Arithmetic &amp; Logic Unit</b>	<b>Hours:05</b>
Number representation. Sequential multiplication, Booths' algorithm for multiplication, integer division.		
<b>Text Book:</b> 1. "Computer Organization" 5th Edition by V. Carl Hamacher & S. Zaky, McGraw-Hill (ISE). 2. "Computer Organization and Architecture: Designing for Performance", 10th Edition by William Stallings, Pearson Education.		
<b>Reference Books:</b> 1. "Computer Organization and Design: The Hardware/Software Interface", 5th Edition by David A. Patterson and John L. Hennessy, Elsevier. 2. "Computer Architecture and Organization", 3rd Edition by John P. Hayes, WCB/McGraw-Hill. 3. Design and Architecture", 2nd Edition by Vincent P. Heuring and Harry F. Jordan, Pearson Education. 4. "Structured Computer Organization", 5th Edition by Tenanbaum A.S., Pearson Education.		

Course Code: 4IT212PC	Course Title: Data Structures	LTPC: L-0 T-0 P-2 C-1
Course Prerequisite: Basics of programming Language & Logic Building Skills		
Course Objectives:	<ol style="list-style-type: none"> <li>1. To understand linear and nonlinear data structures, their memory representations, and real-world data representation techniques.</li> <li>2. To perform operations on data structures such as insertion, deletion, searching, and traversing.</li> <li>3. To learn various data searching and sorting methods along with their complexities.</li> </ol>	
Course Outcomes (Expected Outcome):	<p>On completion of the course, the students will be able to:</p> <ol style="list-style-type: none"> <li>1. Apply various data structures, their operations, algorithmic complexity, string processing techniques and pattern matching algorithms.</li> <li>2. Investigate linear and multidimensional arrays and various operations on it.</li> <li>3. Examine different types of linked list, their memory representation and operations.</li> <li>4. Analyze stack and queues data structures, their operations and applications.</li> <li>5. Evaluate tree-based data structure such as binary tree, binary search trees, heaps etc.</li> <li>6. Design graph and graph traversal and various sorting techniques.</li> </ol>	

#### **List of Experiments:**

The following list is an indicative list, and the subject teacher is free to design his/her own list of experiments based on the syllabus of Data Structure (4IT201PC). Students are advised to explore the Virtual Labs Developed by Ministry of Human Resource Development Government of India available at <http://www.vlab.co.in/broad-area-computer-science-and-engineering>. **Minimum 10 experiments** are to be performed covering the entire syllabus. At least two experiments should be beyond syllabi based on learning of syllabi.

1. Write a program to study and implement the following operations on arrays:
  - a. Create an array
  - b. Insert an element into an array
  - c. Delete an element from an array
  - d. Search an element in an array
  - e. Display the array in Forward Direction
  - f. Display the array in Backward Direction
2. Write a program to traverse an array and find the sum and average of data elements from an array.
3. To study and implement various searching method:
  - a. Linear search method.
  - b. Binary Search method.
4. To study and execute the Pattern matching Algorithms (Slow and Fast)

5. To study and execute various sorting methods:

- a. Selection Sort
- b. Insertion Sort
- c. Merge Sort
- d. Quick Sort
- e. Heap Sort

6. To study and implement various operations on singly linked list.

- a. Traversing the linked list.
- b. Insert a node at the front of the linked list.
- c. Delete a last node of the linked list.
- d. Searching a Linked list.

7. To study and implement the following operations on the doubly linked list.

- a. Insert a node at the front of the linked list.
- b. Insert a node at the end of the linked list.
- c. Delete a last node of the linked list.
- d. Delete a node before specified position.

8. To study and implement the following operations on the circular linked list.

- a. Insert a node at the end of the linked list.
- b. Insert a node before specified position.
- c. Delete a first node of the linked list.
- d. Delete a node after specified position.

9. Understand the stack structure and execute the push, pop operation on it using array implementation.

10. Understand the Queue structure and execute the insertion, deletion operation on it using array implementation..

11. Formulate and demonstrate Transforming Infix Expressions to Postfix Expression using Stack.

12. Formulate and demonstrate the Evaluation of Postfix Expression using Stack.

13. Understand the Tree structure and implement the Pre-order, In-order, post-order traversing operations on it.

14. Understand the concept of Recursion and write a program to calculate factorial of a number using Recursion.

15. Write a Program to implement the concept of BFS algorithm.

16. Write a Program to implement the concept of DFS algorithm.

17. To study and execute Josephus problem.

<b>Course Code: 4IT213PC</b>	<b>Course Title: Data Communication &amp; Networking</b>	<b>L-0 T-0 P-2 C-1</b>		
<b>Course Pre-requisite:</b> Basic understanding of Computer Systems and Architecture, Fundamentals of Programming and Knowledge of Digital Electronics				
<b>Course Objectives:</b>	<ol style="list-style-type: none"> <li>1. To provide a comprehensive understanding of computer networks, their architecture, and protocols.</li> <li>2. To enable the practical implementation and analysis of data communication techniques like digital and analog conversion.</li> <li>3. To equip students with skills to design, simulate, and analyze flow control, switching mechanisms, and network device operations</li> </ol>			
<b>Course Outcomes:</b>	<ol style="list-style-type: none"> <li>1. Illustrate the fundamental concepts of data communication, including components, types of data flow, and communication systems</li> <li>2. Compare and contrast the OSI and TCP/IP models by analyzing the functionalities and protocols at each layer.</li> <li>3. Use knowledge of signal conversion methods and apply error detection and correction techniques to ensure reliable communication</li> <li>4. Analyze IPv4/IPv6 protocols, and packet delivery processes</li> <li>5. Inspect TCP and UDP features, use cases, and transport layer mechanisms.</li> <li>6. Synthesize application layer services like DNS and HTTP and troubleshoot networking issues using protocols and addressing schemes</li> </ol>			
Practical based on the syllabus of Data Communication & Networking (4IT202PC)				
<p>Following list is an indicative list and the subject teacher is free to design his/her own list of experiments based on the syllabus of Data Communication &amp; Networking Lab (4IT205PC)</p> <ol style="list-style-type: none"> <li>1. To study computer Networks and Its topology.</li> <li>2. To study and implement digital –to- digital conversion, analog-to-digital conversion, digital to analog conversion</li> <li>3. To implement and check flow control in DLL</li> <li>4. To study and Implement Asynchronous Protocols</li> <li>5. To Study and Implement synchronous Protocols</li> <li>6. To implement packet switching in network</li> <li>7. To implement Circuit switching in network</li> <li>8. To Demonstrate and study working of various networking devices like switch, router etc.</li> </ol> <p>Students are advised to explore the Virtual Labs Developed by Ministry of Human Resource Development Government of India available at <a href="http://www.vlab.co.in/broad-area-computer-science-and-engineering">http://www.vlab.co.in/broad-area-computer-science-and-engineering</a></p>				

<b>Course Code: 4IT214MD</b>	<b>Course Title: Introduction to Operating Systems</b>	<b>LTPC: L-2 T-0 P-0 C-2</b>
Course Prerequisite:	Discrete Structures, Data Structure, Any programming Language	
Course Objectives:	1. To Understand the fundamental concepts of operating systems 2. To Explore process synchronization and deadlock management 3. To Analyze memory management techniques and virtual memory	
Course Outcomes (Expected Outcome):	On completion of the course, the students will be able to 1. Explain and apply process scheduling algorithms 2. Solve synchronization and deadlock-related issues 3. Investigate memory management techniques	
<b>Unit I:</b>	<b>Introduction to OS &amp; Process Scheduling</b>	<b>Hours: 9</b>
Introduction: Operating System definition, Components and Services, Process Concept, Process Scheduling, Operations on Processes, Inter-process Communication. Process Scheduling: Basic Concepts, Scheduling Criteria, Scheduling Algorithms: Pre-emptive and Non pre-emptive FCFS, SJF, Priority, RR, Multilevel Queue Scheduling, Multilevel Feedback Queue Scheduling		
<b>Unit II:</b>	<b>Process Synchronization</b>	<b>Hours: 9</b>
Process Synchronization Basics: The Critical-Section Problem, Semaphores, Monitors. Deadlocks: Definition & Characterization, Deadlocks Prevention, Avoidance, Detection and Recovery from Deadlock.		
<b>Unit III:</b>	<b>Memory Management</b>	<b>Hours: 9</b>
Memory Management Background, Swapping, Contiguous Memory Allocation Schemes, Paging, Segmentation, Virtual Memory Management: Background, Demand paging scheme, Process Creation, Page Replacement Policies.		
Textbook: 1. Avi Silberschatz, P. B. Galvin, G. Gagne: "Operating System Concepts" (9/e) John-Wiley & Sons.		
Reference Books: 1. A.S. Tanenbaum "Modern Operating Systems" Pearson Education. 2. William Stallings "Operating Systems" Prentice-Hall. 3. D. M. Dhamdhere "Operating Systems" Tata McGraw-Hill. 4. P. Balkrishna Prasad: "Operating Systems" Scitech Publications (I) Pvt.		

<b>Course Code:4IT215VS</b>	<b>Course Title: Computer Skills - I</b>	<b>LTPC: L-1, T-0, P-2, C-2</b>
<b>Course Prerequisite:</b>	Basic knowledge of any Programming Language	
<b>Course Objectives:</b>	<p>Throughout the course, students will be expected to demonstrate their understanding of PHP by being able to do each of the following:</p> <ol style="list-style-type: none"> <li>1. To be able to understand basics of Web Designing.</li> <li>2. To understand data and information processing techniques in Internet and WWW.</li> <li>3. To Design a Web Pages using elements of HTML.</li> <li>4. To be able to design web applications using CSS.</li> <li>5. Basic understanding of various aspects of web technologies Java Script</li> <li>6. To be able to design web applications using PHP programming.</li> </ol>	
<b>Course Outcomes (Expected Outcome):</b>	<p>On completion of the course, the students will be able to:</p> <ol style="list-style-type: none"> <li>1. Apply the principle of Web design in Designing Website.</li> <li>2. Make use of the basic elements of HTML in web design.</li> <li>3. Examine the basic concept of HTML.</li> <li>4. Investigate basics concept of CSS in Web Design.</li> <li>5. Develop the concept of web publishing using Java Script.</li> <li>6. Design HTML Form with PHP.</li> </ol>	
<b>Unit I:</b>	<b>Introduction to Web Design</b>	<b>Hours: 05</b>
Introduction to Internet, WWW and Web 2.0, Web protocols and Web servers, Web Design Principles and Web site structure, Basic principles involved in developing a web site.		
Brief History of Internet, What is World Wide Web?, Why create a web site, Web Standards, Audience requirement.		
<b>Unit II:</b>	<b>Introduction to HTML</b>	<b>Hours: 05</b>
What is HTML, HTML Documents, Basic structure of an HTML document, Creating an HTML document, Markup Tags, Heading-Paragraphs, Line Breaks, HTML Tags.		
<b>Unit III:</b>	<b>Elements of HTML</b>	<b>Hours: 05</b>
Elements of HTML: Introduction to elements of HTML, Working with Text, Working with Lists, Tables and Frames, Working with Hyperlinks, Images and Multimedia, Working with Forms and controls.		
<b>Unit IV:</b>	<b>Introduction to Cascading Style Sheets (CSS)</b>	<b>Hours: 05</b>
Basics of CSS, CSS properties for manipulating texts, background, colors, Gradients, Shadow Effects, borders, margins, paddings, transformations, transitions and animations, etc., CSS box modal and CSS Flex, Positioning systems of CSS, CSS media queries.		
<b>Unit V:</b>	<b>JavaScript</b>	<b>Hours: 05</b>
Basics of JavaScript and Client-side scripting language, JavaScript syntaxes for variables, functions, branches and repetitions. JavaScript alert, prompt and confirm. Objects in JavaScript, Access/Manipulate web browser elements using DOM Structure, forms and validations, JavaScript events		
<b>Unit VI:</b>	<b>PHP</b>	<b>Hours: 05</b>
Introduction to PHP and its syntax, combining PHP and HTML, understanding PHP code blocks like Arrays, Strings, Functions, looping and branching, file handling, processing forms on server side, cookies and sessions.		

**Reference Books:**

1. Black Book, HTML 5, Dreamtech Press
2. Black Book, Web Technologies, Dreamtech Press.
3. Ralph Moseley and M. T. Savaliya, Developing Web Applications, Wiley-India.
4. Sebesta, Programming world wide web, Pearson Education,2007.
5. Dietel and Nieto, Internet and World Wide Web – How to program by PHI/ Pearson EducationAsia.
6. Steven Holzner, PHP: The Complete Reference TataMcGraw-Hill.
7. Steven M. Schafer, HTML, XHTML, and CSS Bible, 5ed Wiley India.
8. John Duckett, Beginning HTML, XHTML, CSS, and
9. JavaScript, Wiley India
10. Ian Pouncey and Richard York, Beginning CSS: Cascading Style Sheets for Web Design Wiley India
11. Kogent Learning, Web Technologies: HTML and Javascript, Wiley India

**Minimum Ten to Twelve experiments / programming assignments must be completed based on the above syllabus covering each of the units.**

<b>Course Code: 4IT216OE</b>	<b>Course Title: Intellectual Property Right</b>	<b>LTPC: L-2, T-0, P-0, C-2</b>
<b>Course Prerequisite:</b>	Basic Knowledge of English and Report Writing	
<b>Course Objectives:</b>	<ol style="list-style-type: none"> <li>1. Impart awareness on Intellectual Property Rights (IPR) and various regulatory issues related to IPR</li> <li>2. Familiarize students with the shades of Intellectual Property Rights (IPR)</li> <li>3. Awareness on intellectual property rights and various regulatory issues related to IPR.</li> </ol>	
<b>Course Outcomes (Expected Outcome):</b>	<p>After successfully completing the course, students will be able to-</p> <ol style="list-style-type: none"> <li>1. Describe the concept of intellectual property and its role.</li> <li>2. Explain the process of obtaining a patent and Industrial Design.</li> <li>3. Apply the concept of copyright laws, trademark classification and criteria for registration.</li> </ol>	
<b>Unit I:</b>	<b>Introduction to Intellectual Property Rights</b>	<b>Hours: 09</b>
Introduction to Intellectual Property Rights: Role of IP in the Economic and Cultural Development of the Society, IP Governance. IP as a Global Indicator of Innovation, Origin of IP, History of IP in India. Patents, Copyrights and Related Rights, Trademarks, Trade Secrets		
<b>Unit II:</b>	<b>Patent and Industrial Design</b>	<b>Hours: 09</b>
<p><b>Patents:</b> Conditions for Obtaining a Patent Protection, To Patent or Not to Patent an Invention, Rights Associated with Patents, Enforcement of Patent Rights, Inventions Eligible for Patenting, Non-Patentable Matters, Patent Infringements, Process of Patenting.</p> <p><b>Industrial Design:</b> Eligibility Criteria, Acts and Laws to Govern Industrial Designs, Design Rights, Enforcement of Design Rights, Non-Protectable Industrial Designs India, Protection Term, Procedure for Registration of Industrial Designs, Importance of Design Registration</p>		
<b>Unit III:</b>	<b>Copyright and Trademarks</b>	<b>Hours: 09</b>
<p><b>Copyrights:</b> Classes of Copyrights, Criteria for Copyright, Ownership of Copyright, Copyrights of the Author, Copyright Infringements, Liability of Owner of an Auditorium/Hall, Fair Use Doctrine, Non-Copyright Work, Copyright Registration, Judicial Powers of the Registrar of Copyrights, Fee Structure, Copyright Symbol, Validity of Copyright.</p> <p><b>Trademarks:</b> Eligibility Criteria, Who Can Apply for a Trademark, Acts and Laws, Designation of Trademark Symbols, Classification of Trademarks, Registration of a Trademark is Not Compulsory, Validity of Trademark, Types of Trademarks Registered in India</p>		

**Textbook:**

1. Prof. Rupinder Tewari and Ms. Mamta Bhardwaj, "Intellectual Property: A Primer for Academia" by, Publication Bureau, Panjab University, Chandigarh, 2021.
2. K. V. Nithyananda, "Intellectual Property Rights: Protection and Management", by Cengage Learning India Private Limited, 2019.

**Reference Books:**

1. Deborah E. Bouchoux, "Intellectual Property for Paralegals – The Law of Trademarks, Copyrights, Patents & Trade Secrets", 4th Edition, Cengage Learning, 2012.
2. N. S. Gopalakrishnan and T. G. Agitha, "Principles of Intellectual Property", Eastern Book Company, Lucknow, 2009.
3. M. M. S. Karki, "Intellectual Property Rights: Basic Concepts", Atlantic Publishers, 2009.
4. Ganguli Prabuddha, "Intellectual Property Rights--Unleashing the Knowledge Economy", Tata McGraw Hill, 2001.

Course Code: 4IT216OE	Course Title: Artificial Intelligence	LTPC: L-2 T-0 P-0 C-2
Course Prerequisite:	Professional Communication Skill and Computer Fundamentals	
Course Objectives:	<ol style="list-style-type: none"> <li>1. Introduce the fundamental concepts of Artificial Intelligence.</li> <li>2. Familiarize students with problem-solving, search strategies, and AI applications.</li> <li>3. Develop understanding of knowledge representation, reasoning, and machine learning basics.</li> <li>4. Equip students to design simple AI-based applications.</li> </ol>	
Course Outcomes (Expected Outcome):	<ol style="list-style-type: none"> <li>1. Understand the basics and scope of Artificial Intelligence.</li> <li>2. Apply search strategies for solving real-world problems.</li> <li>3. Represent knowledge using logic and inference techniques.</li> </ol>	
Unit I:	Introduction to Artificial Intelligence	Hours: 09
Definition, History, and Evolution of AI, Types of AI: Narrow, General, and Super AI, Applications of AI in real life: Robotics, Healthcare, Education, Business, Foundations and Philosophy of AI, Intelligent Agents and Environments		
Unit II:	Problem Solving and Search Strategies	Hours: 09
Problem Solving as Search, Uninformed Search: BFS, DFS, Informed Search: Best-first, A*, Heuristic functions, Game Playing: Minimax Algorithm, Alpha-Beta Pruning, Constraint Satisfaction Problems (CSP)		
Unit III:	Knowledge Representation and Introduction to Machine Learning	Hours: 09
Knowledge Representation Techniques: Propositional and Predicate Logic, Inference Mechanisms: Forward and Backward Chaining, Basics of Machine Learning: Supervised, Unsupervised, Reinforcement Learning, Simple ML Algorithms: Decision Trees, k-NN, Applications of Machine Learning and AI Ethics.		
<b>Text Book:</b> <ol style="list-style-type: none"> <li>1. "Artificial Intelligence – A Modern Approach", Stuart Russell and Peter Norvig Pearson Education</li> </ol>		
<b>Reference Books:</b> <ol style="list-style-type: none"> <li>1. "Artificial Intelligence" Elaine Rich, Kevin Knight, and Shivashankar B. Nair, Tata McGraw Hill</li> <li>2. "Introduction to Artificial Intelligence", Wolfgang Ertel, Springer</li> <li>3. "Machine Learning for Absolute Beginners", Oliver Theobald, Scatterplot Press</li> </ol>		

<b>Course Code:4IT216OE</b>	<b>Course Title: E-Commerce</b>	<b>LTPC: L-2 T-0 P-0 C-2</b>		
Course Prerequisite:	Basic understanding of business concepts, internet technology, and general knowledge of commerce.			
Course Objectives:	<ol style="list-style-type: none"> <li>1. Understand e-commerce basics, significance, and models (B2B, B2C, C2C, C2B).</li> <li>2. Assess the advantages, challenges, and trends in e-commerce and e-business.</li> <li>3. Learn e-business strategies and analyze the e-commerce landscape in India.</li> </ol>			
Course Outcomes (Expected Outcome):	<ol style="list-style-type: none"> <li>1. Gain a clear understanding of e-commerce fundamentals, its models, and their significance.</li> <li>2. Analyze the advantages, challenges, and trends in e-commerce and e-business.</li> <li>3. Apply e-business strategies and assess the e-commerce landscape in India.</li> </ol>			
<b>Unit I: Introduction to E-Commerce</b>	<b>Hours: 9</b>			
Meaning and concept of Electronic Commerce, Significance of Electronic Commerce, Scope of E-Commerce, Functions of Electronic Commerce, Electronic Commerce Application, Advantages and disadvantages of E-Commerce, Electronic Commerce V/S Traditional Commerce, Prospectus of Electronic Commerce in India				
<b>Unit II: Electronic Commerce Revolution</b>	<b>Hours: 9</b>			
The birth of Portals, Major Modes found in E-Commerce, Matrix of E-Commerce Models, E-business Models, Business-to-Customer (B2C), Business to Business (B2B), Consumer to Consumer(C2C), Consumer to Business (C2B)				
<b>Unit III: E-Business</b>	<b>Hours: 9</b>			
Definition of Electronic Business, Evolution of Electronic Business Applications, Emerging Applications, Electronic Business Architecture, Electronic Business(e-b) initiatives, Problems of the E-Business, Electronic Business Implementation, Concept of E-Customer Relation Management, The Indian Scenario for Electronic Business				
<b>Textbooks:</b>				
1. E-Commerce: Concepts, Models, Strategies C.S.V. Murthy, Himalaya Publishing House				
<b>Reference Books:</b>				
1. Agrawal K.N. & Deeksha Agrawal: Business on the Net; What's & How's of E-Commerce- Macmillan New Delhi.				
2. Electronic Commerce- By Bharat Bhaskar, Tata McGraw Hill Publication, New Delhi.				
3. Parag Diwan & Sunil Sharma: E-Commerce-A Manager's Guide to E-Business; Excel Books, New Delhi.				
4. Minoli and Minal: Web Commerce Technology Books Tata Mc Graw Hill, New Delhi				

<b>Course Code:4IT217EM</b>	<b>Course Title: IT Ethics and Management</b>	<b>LTPC: L-2 T-0 P-0 C-2</b>
Course Prerequisite:		
Course Objectives:	<ol style="list-style-type: none"> <li>1. To enable the students to create an awareness of engineering and professional ethics</li> <li>2. To instill moral, social values and appreciate the rights of others</li> <li>3. To regulate the student's behavior in a professional environment</li> <li>4. To be conscious about the impact of non-ethical engineering decisions</li> <li>5. To comprehend 'mind and desire control' needs for being ethical</li> </ol>	
Course Outcomes (Expected Outcome):	<p>On completion of the course, the students will be able to</p> <ol style="list-style-type: none"> <li>1. Relate ethical and non-ethical situations</li> <li>2. Examine the moral judgment &amp; correlate the concepts in addressing the ethical dilemmas</li> <li>3. Identify risk and safety measures in Computers, Software and Digital Information</li> </ol>	
<b>Unit I: Introduction to Ethics</b>	<b>Hours: 9</b>	
Senses of Engineering and professional ethics, Engineering profession & its view, Ethical issues for engineers, distinction between ethics, morals and laws, opinions vs. judgments, Ethical theories: utilitarianism, duty, right, virtue; Cost-benefit analysis in engineering, McCuen's ethical dimensions, IEEE: Code of conduct & Code of ethics		
<b>Unit II: Professional Practices in Engineering</b>	<b>Hours: 9</b>	
Professional attributes, Difference in engineering and other professions; Ethical dilemma: right-wrong or better worse; Code of ethics for engineers in India: need and its roles; abuse of codes, ethical relativism, well-being and profession, Ethics as Design - Doing Justice to Moral Problems, Kohlberg's theory – Gilligan's theory		
<b>Unit III: Computers, Software and Digital Information</b>	<b>Hours: 9</b>	
Emergence of Computer ethics, issues in Computer ethics: distribution of power issues, property issues, issues of privacy, professional issues, Computer crimes, Computer Software and Digital Information: Characteristics of digital information, s/w as IP, and challenges in information age, IEEE code of conduct and code of ethics		
<b>Textbooks:</b>		
1. Prof. Susmita Mukhopadhyay, 'Ethics in Engineering Practice' IIT Kharagpur 2. Mike Martin and Roland Schinzinger, 'Ethics in Engineering', Tata McGraw Hill, New York, 2005		
<b>Reference Books:</b>		
1. Charles E. Harris, Michael S. Pritchard and Michael J. Rabins, 'Engineering Ethics – Concepts and Cases', Cengage Learning, 2009 & Thompson Learning, 2000 2. Govindarajan M., Natarajan, 'Engineering Ethics', Prentice Hall of India, New Delhi, 2004 3. Stephen Byars, 'Business Ethics', USC Marshall School of Business Kurt Stanberry, University of Houston ( <a href="https://openstax.org/details/books/business-ethics">https://openstax.org/details/books/business-ethics</a> )		